**Reg. No. \_\_\_\_\_\_\_\_**

**Karunya University**

**(Karunya Institute of Technology and Sciences)**

(Declared as Deemed to be University under Sec.3 of the UGC Act, 1956)

**Supplementary Examination - June 2011**

**Subject Title: SYSTEM SOFTWARE Time: 3 hours**

**Subject Code: CS250 Maximum Marks: 100**

#### **Answer ALL questions**

**PART – A (10 x 1 = 10 MARKS)**

1. Define system software.

2. List out any two CISC and RISC machine.

3. Define the basic functions of assembler.

4. What is the need of SYMTAB (symbol table) in assembler?

5. Define dynamic linking.

6. Define loader.

7. What are known as nested macro call?

8. Define macro processor.

9. What are the methods in Interaction language of a text editor?

10. Name some of text editors.

**PART – B (5 x 3 = 15 MARKS)**

11. Mention the differences between SIC and SIC/XE.

12. Explain in detail about basic assembler functions.

13. Discuss in detail about Machine-independent Loader features.

14. Explain about Macro Processor Design options.

15. Explain in detail about debugging.

**PART – C (5 x 15 = 75 MARKS)**

16. Explain the architecture of SIC/XE machine.

(OR)

17. Explain about the Cray T3E architecture.

18. Discuss in detail about the machine-Independent Assembler features.

(OR)

19. Write the **Algorithm for Pass 1 assembler and Pass 2 assembler:**

**20.** What is Dynamic Linking? Explain in detail about the same.

(OR)

21. Explain in detail about [Linkage Editors.](http://www.enggedu.com/tamilnadu/university_questions/question_answer/be_am_2008/3th_sem/cse/CS1203/part_b/13_b_1.html)

**22.** What are the different features of machine-independent Macro processor? Explain in detail any two of them.

(OR)

23. Explain with an example how macro processor allows the invocation of macro with in macro.

24. Explain about the editor structure.

(OR)

25. Discuss in detail about debugging functions and capabilities.